

<b>Notice of References Cited</b>	Application/Control No. 10/707,388		Applicant(s)/Patent Under Reexamination DIVAKARUNI ET AL.	
	Examiner Steven J. Fulk		Art Unit 2891	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,168,984	01-2001	Yoo et al.	438/239
*	B	US-5,218,222	06-1993	Roberts, Gregory N.	257/362
	C	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
*	U	Wolf, "Silicon Processing for the VLSI Era: Process Integration," Vol. II, 1990, pp. 146, 176, 193.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.